



uninord

Uninord is specialized in providing digital ASIC, FPGA and embedded software design services. We have a thorough understanding about the whole product design process. With our design expertise customer will get help in different design phases and reduce time-to-market in product development process.

Now we're offering a chance to join our team of experienced professionals. Industrial work experience is desirable, but we put more value on motivation and the ability to learn new things. You should have good communication, negotiation and teamwork skills, and be fluent in both written and spoken English.

We are looking for both a

Master Thesis worker for Digital IC/FPGA tool development

to work permanently as a part of our team at Oulu.

Tasks will include user interface design for modular design block integration tool. Based on your experience the work will contain also other tasks in design flow.

Needed skills for assignment:

- User interface design
- Understanding of modular SW design
- Basic digital design understanding

Background that would help in this position:

- Good mathematical background
- Digital design experience with VHDL or Verilog
- Some experience with Matlab
- Experience with design and simulation tools
- Experience in any coding or scripting language

For further information on these positions, please contact Mr. Jarmo Niskanen, +358 40 730 8491. To apply, please send your application, including CV and transcript of records, to careers@uninord.fi by February 22nd, 2019.